

NUMAO, T.  
Appl. No. 10/656,299  
July 25, 2006

**AMENDMENTS TO THE DRAWINGS**

The attached sheets of drawings include changes to Figs. 23-26. These sheets, which include Figs. 23-26, replace the original sheets including Figs. 23-26. In particular, Figs. 23-26 have been labeled “prior art.”

Attachment: 3 Replacement Sheets

**REMARKS**

This is in response to the Office Action dated April 27, 2006.

The drawings have been amended as requested by the Examiner. In particular, Figs. 23-26 have been labeled prior art.

**Allowable Subject Matter**

Applicant notes with appreciation the Examiner's allowance of claims 1-7, and the Examiner's indication that claim 14 contains allowable subject matter. In this regard, allowable claim 14 has been rewritten in independent form. Claim 14 has also been amended so that numbers (second, third, forth, etc.) are not skipped. Thus, claims 1-7 and 14 are now in condition for allowance given the Examiner's indication of allowable subject matter.

**Formalities**

It is respectfully submitted that the formality objections to claim 8 have been addressed and overcome by the changes to claim 8 herein. Claim 8 has been amended to state that the control terminals of the second active devices (e.g., Q10) are connected to the second set of lines (e.g., Gi).

**Claim 8 – Art Rejection**

Claim 8 stands rejected under 35 U.S.C. Section 102(e) as being allegedly anticipated by Koyama. This Section 102(e) rejection is respectfully traversed for at least the following reasons.

Claim 8 as amended requires “second active devices provided between the first set of lines and the first capacitors, and wherein control terminals of the second active devices are connected to the second set of lines; first switching devices provided between the first set of lines and current output terminals of the first active devices; and a third set of lines connected to

control terminals of the first switching devices.” For purposes of example and without limitation, Fig. 9 of the instant application illustrates that control terminals of the second active devices (Q10) are connected to the second set of lines (gate lines Gi), whereas control terminals of the first switching devices (Q1) are connected to a third set of lines (Wi). In other words, claim 8 as amended requires that the control terminals of the second active devices (e.g., Q10) and the first switching devices (e.g., Q1) are connected to different lines (e.g., see Fig. 9).

Koyama fails to disclose or suggest the aforesaid quoted features of claim 8. Instead, Koyama teaches away from this because in Fig. 9 of Koyama the control terminals (e.g., gates) of the alleged first switching devices Tr4 and second active devices Tr3 are connected to the *same* line Gaj. Thus, Koyama is entirely unrelated to the invention of amended claim 8.

#### Claim 9 – Art Rejection

Claim 9 requires “a third set of lines provided in parallel with the first set of lines; first active devices provided in series with the electro-optic devices, the *first active devices and the electro-optic devices being provided between a power source line and an opposing electrode*; first capacitors connected to control terminals of the first active devices; second active devices provided between the third set of lines and the first capacitors; and first switching devices provided between the first set of lines and current output terminals of the first active devices.” Thus, claim 9 has been amended to make clear that the third set of lines and the power source line(s) are different. For purposes of example and without limitation, Fig. 17 of the instant application illustrates first set of lines Sj, second set of lines Gi, third set of lines Tj provided in parallel with the first set of lines Sj, and *power source lines Vref which are different than the third set of lines*. Fig. 17 also illustrates, for example and without limitation, first switching

devices (Q1) connected to the first set of lines S<sub>j</sub> whereas second active devices (Q14) are connected to the third set of lines T<sub>j</sub>.

In contrast with claim 9, Koyama in Fig. 9 illustrates that the first switching devices (Tr4) are connected to the lines (S<sub>i</sub>) whereas the second active devices (Tr5) are connected to the power source lines (V<sub>i</sub>). The amendment to claim 9 makes it clear that the third set of lines and the power source line(s) are different. Because the third set of lines is different from the power source line(s) according to amended claim 9, Koyama cannot meet the invention thereof.

#### Claim 15

Claim 15 requires *means for selectively coupling one of the first set of lines to a corresponding one of the third set of lines*. For example, Fig. 17 of the instant application illustrates first set of lines S<sub>j</sub>, second set of lines G<sub>i</sub>, and third set of lines T<sub>j</sub> that is provided parallel to the first set of lines S<sub>j</sub>. Fig. 17 also illustrates first active devices Q4, first capacitors C1, second active devices Q14 and first switching devices Q1. The Fig. 17 embodiment further includes means for selectively coupling one of the first set of lines to a corresponding one of the third set of lines when H<sub>j</sub> is set HIGH (e.g., pg. 64, lines 8-14; pg. 67, lines 21-22).

Koyama fails to disclose or suggest this italicized feature of claim 15. In particular, in Fig. 9 of Koyama, lines S<sub>i</sub> and V<sub>i</sub> are not selectively coupled to each other, and never would be. Claim 15 defines over the cited art in this respect.

#### Conclusion

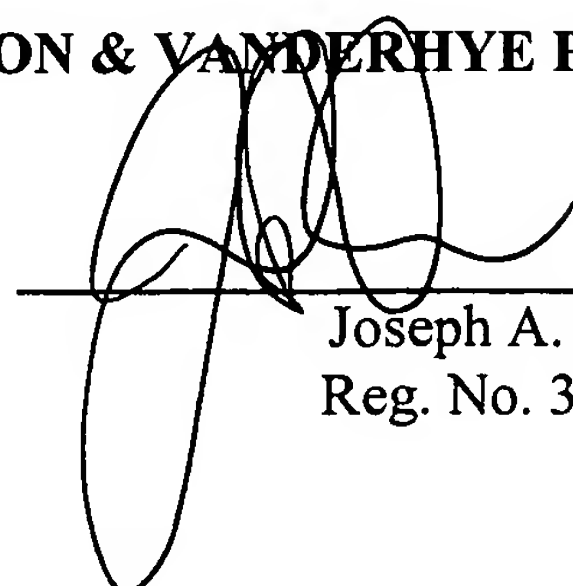
It is respectfully requested that all rejections be withdrawn. All claims are in condition for allowance. If any minor matter remains to be resolved, the Examiner is invited to telephone the undersigned with regard to the same.

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Respectfully submitted,

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By:



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